

Notice of References Cited

Application/Control No.

09/620,474

Applicant(s)/Patent Under
Reexamination
FUJIWARA, MAKOTO

Examiner

Ayal I Sharon

Art Unit

2123

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,541,849	07-1996	Rostoker et al.	716/18
	B	US-6,493,351	12-2002	Shideler, Ronald K.	370/438
	C	US-6,516,379	02-2003	Deshpande et al.	710/310
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Mahmud, S.M. "Communication Performance in a Hierarchical Bus System". Int'l Symposium on Circuits and Systems, 1989. May 11, 1989. Vol.1, pp.122-125.
	V	Mahmud, S.M. "Performance Analysis of Multilevel Bus Networks for Hierarchical Multiprocessors". IEEE Transactions on Computers. July 1994. Vol.43, Issue 7, pp.789-805.
	W	Chang, Y.C. et al. "Design and Performance of a Highly Pipelined Bus for Shared Memory Multiprocessor". Proceedings of the 20th EUROMICRO Conference. Sept. 8, 1994. pp.451-456.
	X	Chiung-San, L. et al. "Performance Modelling and Evaluation for the XMP Shared-Bus Multiprocessor Architecture". Int'l Conf. on Parallel and Distributed Systems. Dec. 21, 1994. pp.446-453.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Woo-Jong, H. et al. "A Multiprocessor Server with a New Highly Pipelined Bus." Proceedings of the 10th Int'l Parallel Processing Symposium, (IPPS '96). Apr. 19, 1996. pp.512-517.
	V	Jiang, H. et al. "PPMB: A Partial-Multiple-Bus Multiprocessor Architecture with Improved Cost-Effectiveness". IEEE Transactions on Computers. Mar. 1992. Vol. 41, Issue 3, pp.361-366.
	W	
	X	

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Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.